

# TYLER SEIDEN SHERMAN

TylerSSherman98@gmail.com | (914) 826-7625 | Tyler-S-Sherman.com

## WORK EXPERIENCE

---

**Alarm.com**, Tysons, VA

*Technical Product Manager III – Video*

**June 2023 – Present**

**SpaceX**, Hawthorne, CA

*Electrical Hardware Product Engineer II*

**December 2021 – May 2023**

- Provide extreme technical ownership and product management for fault tolerant, mixed signal avionics across all phases of the product life cycle including Flight Computers, Flight Termination System (FTS) units, and RF devices
- Creative problem solver capable of working within a multidisciplinary team to see projects through to completion
- Design high reliability hardware to expand capabilities and perform benchtop and environmental validation testing
- Triage production nonconformities, root cause anomalies, and conduct environmental qualification campaigns
- Led the Design for Excellence (DFX) initiative to improve the procurement, manufacturability, and reliability of all flight avionics, spanning from piece parts and PCBs through PCBAs and electromechanical assemblies
  - Implemented and reviewed changes to electrical schematics, PCB layouts, and mechanical drawings
  - Developed new processes to increase efficiency and organization of design updates, doubling YOY release rate

*Electrical Hardware Product Engineer I*

**June 2020 – December 2021**

- Designed and certified a high reliability, reusable GPS LNA to replace a COTS unit, reducing costs by 70%
- Devised and executed plan to replace COTS connectors with an in-house design, saving over \$2M annually
- Drove process to identify and resolve component issues caused by shortages and obsolescence for all flight parts
- Restructured product documentation with SQL automation to improve accuracy and increase data accessibility

**Lockheed Martin**, Owego, NY, *Electrical & Computer Hardware Engineering Intern*

**Summer 2019**

- Performed PCBA bring-up, integration, and testing and root caused PCBA defects using electrical test equipment
- Achieved 300% reduction in video testing time using MATLAB and modified circuitry to meet requirements
- Documented test procedures, interfaced with test engineers, and presented to program management

**Cornell University Space Systems Design Studio**, Ithaca, NY, *Researcher*

**February 2018 – May 2020**

- Launched two CubeSats on Virgin Orbit's LauncherOne for the Pathfinder for Autonomous Navigation mission
- Designed and built PCBAs and harnessing for the Attitude Control, Communications, and Docking systems
- Wrote test procedures, integrated avionics into spacecraft assembly, and executed system level testing
- *Power Team Lead (Summer 2018)*: Delegated tasks, developed schedule, and reviewed designs for a team of three

**IgnitionOne**, New York, NY, *Analytics Engineering Intern*

**Summer 2017**

- Queried data to investigate digital marketing performance metrics and provided insights to optimize campaigns
- Created a Python program to parse advertising bid request data and transform it into an analyzable format

## SPECIALIZED SKILLS

---

<b>Analysis</b>	Design for Excellence (DFX), Derating, Root Cause, Sifos Ethernet Tester
<b>Design</b>	Component Selection, Schematic Capture, PCB Layout, Environmental Testing, Project Management
<b>Programming</b>	Git, Stash, Python, SQL, Bash, HTML, CSS, MATLAB
<b>Software</b>	Altium NEXUS, SiliconExpert, Teamcenter, LTspice, Confluence, Jira, Power BI, Microsoft Office

## EDUCATION

---

**Cornell University**, College of Engineering, Ithaca, NY

**May 2020**

Master of Engineering, Electrical & Computer Engineering

GPA: 4.080

*Honors*: Lester B. Knight M.Eng./MBA Scholarship

**Cornell University**, College of Engineering, Ithaca, NY

**December 2019**

Bachelor of Science, Electrical & Computer Engineering

GPA: 3.881

*Programs*: Computer Science Minor, Dyson Business Minor for Engineers, Engineering Leadership Certification

*Honors*: Magna Cum Laude, Dean's List (6/7 semesters), Eta Kappa Nu (IEEE-HKN), Tau Beta Pi